

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	0	(716/16or716/17or716/18).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:57
S2	2189	716/16 or 716/17 or 716/18	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:58
S3	558	S2 and ASIC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:58
S4	124	S3 and (synthesis near2 tool)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:17
S5	0	S4 and (file near2 scripts)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:17
S6	23	S4 and (script near2 files)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:59
S7	0	S4 and ((script near2 files) same automatic\$)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 15:59
S8	23	S6 and (output)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:00
S9	23	S6 and (output) and automatic\$	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:01

S10	8	S6 and (output) and (automatic\$ same script)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:18
S11	1286	(synthesis near2 tool)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:17
S12	0	S11 and (file near2 scripts)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:17
S13	50	S11 and (output) and (automatic\$ same script)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:30
S14	2	"6185726".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:21
S15	19	S11 and (output) and (automatic\$ same script)and (HDL near2 code)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:31
S16	16	S11 and (output) and (automatic\$ same script)and (HDL near2 code)and stages	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:45
S17	61	xml near2 template near2 file	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:46
S18	49	S17 and "24"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:46
S19	0	S17 and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:46

S20	0	S17 and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:46
S21	1091	xml same template	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:46
S22	0	S21 and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:46
S23	0	S21 and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:47
S24	0	S21 same ASC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:47
S25	24	template and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:48
S26	24	template and S4 and designer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:48
S27	12	template and S4 and designer and (automatic\$ same script)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:50
S28	12	ASIC and (automatic\$ with creat\$ with script)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:53
S29	25	ASIC and (automatic\$ with generat\$ with script)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 16:57

S30	17	ASIC and (automatic\$ with script) and (script near2 files)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:00
S31	10	ASIC and (automatic\$ with script) and (command near2 files)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:00
S32	3	ASIC and (automatic\$ with(command near2 files))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:00
S33	341	ASIC and (automatic\$ with generate\$ with (code or program))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:01
S34	24	S33 and ((code or program or script) near2 files)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:09
S35	116	ASIC near2 designer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:09
S36	1	S35 and (automatic\$ same ((script\$ or code) near2 files))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:10
S37	8	ASIC and (automatic\$ same ((script\$ or code) near2 files))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:12
S38	12	ASIC and (automatic\$ with script) and (script near2 files)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:13
S39	5035	ASIC same design\$2	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:14

S40	0	S39 and (scripts same constrains)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:14
S41	6	S39 and (scripts near2 files)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:15
S42	6	S39 and (scripts near2 files) and automatic\$	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:16
S43	44	ASIC and (script near2 files)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/21 17:16
S44	13	ASIC and (script near2 files) and template	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 10:21
S45	16190	707/3 or 707/10 or 707/6 or 707/9 or 713/200	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 10:22
S46	0	S45 and (trap same (robot or spider or scrawler))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 10:23
S47	7	S45 and (prevent\$ same robot)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 10:23
S48	163	(trap same (robot or spider or scrawler))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 10:23
S49	0	S48 and reprevent	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 10:23
S50	119	S48 and prevent\$	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 10:23
S51	0	S50 and (prevent\$3 with robots)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 10:24

S52	1	S50 and session	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 10:24
S53	1	S50 and (session or logon)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 10:24
S54	356	(prevent\$3 with robots)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 10:24
S55	6	S54 and ((web near2 site) or website)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 10:25
S56	1	10/091393	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 10:48
S57	1	10/091393 and (second near2 stage) and (first near2 stage)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:54
S58	0	"7987482".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 12:54
S59	2	"5987482".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 13:14
S60	0	"5987482".pn. and (table with content)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 13:43
S61	0	"6356930".pn. and sumary	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 13:43

S62	1	"6356930".pn. and summary	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 13:44
S63	1	"6356903".pn. and summary	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 13:44
S64	0	(document same outline same sections same summary)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 13:44
S65	123	(document same sections same summary)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 13:46
S66	3	S65 and (table near2 content)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 13:45
S67	71	(document same sections same summary)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/22 14:33
S68	2	"5893109".pn.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 10:05
S69	279	(automatic\$ with generat\$ with script)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 10:06
S70	16	S69 and (synthesis near2 tool)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 10:07
S71	13	S69 and (synthesis near2 tool) and files	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 12:34
S72	2	"5966714".pn.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 10:15

S73	1	"5903475".pn. and libraries	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 12:51
S74	2	"6697880".pn.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 12:52
S75	844	synthesis near2 tool	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 13:03
S76	25	S75 and (automatic\$4 same script)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 12:58
S77	16	S75 and (automatic\$4 with generat\$ with script)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 12:58
S78	279	(automatic\$4 with generat\$ with script)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 12:59
S79	0	S78 and ((receiv\$ with output) with (stage or pharse))	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 12:59
S80	16	S78 and S75	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 13:03
S81	583	synthesis near2 tools	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 13:03
S82	0	S81 and (constraint near2 scripts)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 13:03
S83	11	S81 and (constraint near2 script)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 13:03
S84	0	S81 and ((constraint near2 script) same automatic\$4)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 13:04

S97	231	ASIC and (synthesis near2 tool)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 16:33
S98	132	S97 and (stages or phrases or states)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 16:34
S99	16	S98 and (automatica\$ same script)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 17:21
S100	10	"5991735".pn. or "6101489".pn. or "5974398".pn. or "5793972".pn. or "6101486".pn.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 17:24
S101	8	"5974398".pn. or "6101489".pn. or "5974398".pn. or "5793972".pn. or "6101486".pn.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 17:25
S102	8	"5991735".pn. or "6101489".pn. or "5974398".pn. or "5793972".pn. "5991735".pn.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 17:27
S103	6	"5991735".pn. or "5974398".pn. or "5793972".pn. "5991735".pn.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 17:50
S104	7	Robert.in. and (purchasing near2 options)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 17:29
S105	0	Robert.in. and (purchasing near2 options) and "234"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 17:30
S106	14	705/27 and roberts.in.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 17:30
S107	0	10/242344	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 17:52
S108	2	"10242344"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/12/23 17:52

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"5903475".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 12:19
L2	984	703/6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 12:20
L3	888	703/6	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 12:20
L4	335	703/16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 12:20
L5	71	703/16 and ASIC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 12:21
L6	67	703/16 and ASIC	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 13:08
L7	314	703/16	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 13:08
L8	67	7 and ASIC	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 13:08
L9	5	8 and (automatically with scripts)	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/05 13:09

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced
- ☐ CrossRef

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

IEEE Enterprise

- ☐ Access the IEEE Enterprise File Cabinet



Your search matched **2** of **1108362** documents.

A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.

Refine This Search:

You may refine your search by editing the current search expression or entering a new one in the text box.

☐ Check to search within this result set

Results Key:

JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

1 BRAINS: a BIST compiler for embedded memories

Chuang Cheng; Chih-Tsun Huang; Jing-Reng Huang; Cheng-Wen Wu; Chen-Jong Wey; Ming-Chang Tsai;

Defect and Fault Tolerance in VLSI Systems, 2000. Proceedings. IEEE International Symposium on , 25-27 Oct. 2000

Pages:299 - 307

[\[Abstract\]](#) [\[PDF Full-Text \(448 KB\)\]](#) IEEE CNF

2 C++ based system design of a 72 Mb/s OFDM transceiver for wireless LAN

Verkest, D.; Eberle, W.; Schaumont, P.; Gyselinckx, B.; Vernalde, S.;

Custom Integrated Circuits, 2001, IEEE Conference on. , 6-9 May 2001

Pages:433 - 439

[\[Abstract\]](#) [\[PDF Full-Text \(900 KB\)\]](#) IEEE CNF

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced
- ☐ CrossRef

Member Services

- ☒ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

IEEE Enterprise

- ☐ Access the IEEE Enterprise File Cabinet

Your search matched **5** of **1108362** documents.

A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.

Refine This Search:

You may refine your search by editing the current search expression or entering a new one in the text box.

☐ Check to search within this result set

Results Key:

JNL = Journal or Magazine **CNF** = Conference **STD** = Standard

1 Automating wave-pipelined circuit design

Kim, W.J.; Kim, Y.-B.;

Design & Test of Computers, IEEE, Volume: 20, Issue: 6, Nov.-Dec. 2003

Pages:51 - 58

[\[Abstract\]](#) [\[PDF Full-Text \(287 KB\)\]](#) IEEE JNL

2 BRAINS: a BIST compiler for embedded memories

Chuang Cheng; Chih-Tsun Huang; Jing-Reng Huang; Cheng-Wen Wu; Chen-Jong Wey; Ming-Chang Tsai;

Defect and Fault Tolerance in VLSI Systems, 2000. Proceedings. IEEE International Symposium on, 25-27 Oct. 2000

Pages:299 - 307

[\[Abstract\]](#) [\[PDF Full-Text \(448 KB\)\]](#) IEEE CNF

3 Designing an asynchronous microcontroller using Pipefitter

Blunno, I.; Lavagno, L.;

Very Large Scale Integration (VLSI) Systems, IEEE Transactions on, Volume: 12, Issue: 7, July 2004

Pages:696 - 699

[\[Abstract\]](#) [\[PDF Full-Text \(128 KB\)\]](#) IEEE JNL

4 Designing an asynchronous microcontroller using Pipefitter

Blunno, I.; Lavagno, L.;

Computer Design: VLSI in Computers and Processors, 2002. Proceedings. 2002 IEEE International Conference on, 16-18 Sept. 2002

Pages:488 - 493

[\[Abstract\]](#) [\[PDF Full-Text \(573 KB\)\]](#) IEEE CNF

5 C++ based system design of a 72 Mb/s OFDM transceiver for wireless LAN

Verkest, D.; Eberle, W.; Schaumont, P.; Gyselinckx, B.; Vernalde, S.;

Custom Integrated Circuits, 2001, IEEE Conference on, 6-9 May 2001



[Web](#) [Images](#) [Groups](#) ^{New!} [News](#) [Froogle](#) [more »](#)

ASIC design and synthesis tool and creating script files

Search

[Advanced Search](#)
[Preferences](#)

The "AND" operator is unnecessary – we include all search terms by default. [\[details\]](#)

Web

Results 1 - 6 of about 19 for **ASIC design and synthesis tool and creating script files**. (0.29 seconds)

Did you mean: **ASIC design and *synthesis* tool and creating script files**

Sponsored Links

FPGA FAQ comp.arch.fpga archives - messages from 52425

... Can anyone tell me what is **synthesis scripts**? " > > A synthesis ... T >> << A >> Hi Skillie, In the **ASIC** flow (from ... that you specify along with **design** rules that ...
www.fpga-faq.com/archives/52425.html - 62k - [Cached](#) - [Similar pages](#)

Free ASIC design info

News, articles, whitepapers on SOCs, ASICs, IP, EDA tools.
www.soccentral.com

FPGA FAQ comp.arch.fpga archives - messages from 20550

... In a perfect **ASIC** world, a > designer would use ... to verify the pre and post **synthesis** netlists match ... frequency range (requires a filter = **design**) > and very ...

www.fpga-faq.com/archives/20550.html - 80k - [Cached](#) - [Similar pages](#)

ASIC Design Guide

Online search directory providing information on **ASIC design**
www.finditonline.ws

Verification Guild

... seems to be a requirement for all **ASIC** positions. ... you are on the bleeding edge of **synthesis**, where you ... Verification is very unique to each **design**, it does not ...
verificationguild.com/modules.php?name=Forums&file=viewtopic&p=1816 - 58k - [Cached](#) - [Similar pages](#)

Spike Technologies - Chip Design Experts

... Expertise in Verilog, **synthesis** and iterative timing closure is desired. ... technologies, using the industry's leading Place & Route tools. ... **ASIC Design Engineers**. ...
www.spiketech.com/careers.htm - 33k - Supplemental Result - [Cached](#) - [Similar pages](#)

Schematy serwisowe, projekty, ciekawe rozwiazania, oprogramowanie ...

... I want know if Virtuoso Tool is included in Cadence IC **Design** 5.0 Package ... When **synthesis** it gives error 1. There is no way to find out what could be ...
www.elektroda.pl/eboard/spyder400.html - 99k - Supplemental Result - [Cached](#) - [Similar pages](#)

Linux-Hardware Digest #474

... Is > > there any any **synthesis tool** to use with Linux these ... that you can get a decent CAE tools under Linux ... If your goal is full-blown **ASIC design**, go for ...
www.mail-archive.com/linux-hardware@senator-bedfellow.mit.edu/msg03402.html - 31k - Supplemental Result - [Cached](#) - [Similar pages](#)

In order to show you the most relevant results, we have omitted some entries very similar to the 6 already displayed.

If you like, you can repeat the search with the omitted results included.

Did you mean to search for: **ASIC design and *synthesis* tool and creating script files**



Free! Google Desktop Search: Search your own computer.

ASIC design and synthesis tool a **Search**

[Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)